



520.43306X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): O. SUZUKI et al
Serial No.: 10/722,597
Filed: November 28, 2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
SEMICONDUCTOR INTEGRATED CIRCUIT CHIP THEREOF
Group: 2822
Examiner: M. LEWIS

STATEMENT OF SUBSTANCE OF INTERVIEW

Commissioner For Patents
POB 1450
Alexandria, VA 22313-1450

January 17, 2006

Sir:

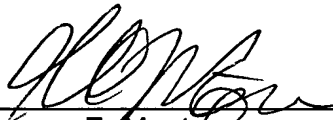
Appreciation is expressed to Examiner Monica Lewis for her courtesy during a telephone call from Examiner Lewis to the below-identified Attorney on January 12, 2006. During this telephone call, Examiner Lewis advised the undersigned Attorney that the original grouping set forth in the Restriction Requirement of September 29, 2005 was incorrect. Specifically, Examiner Lewis indicated that Group I should have included claims 1-14 while Group II should have included claims 15-20. Accordingly, in response, the undersigned Attorney advised Examiner Lewis that the applicants continue to elect Group I, directed to claims 1-14, drawn to a semiconductor integrated circuit chip with a heat transfer layer comprising a flow passage, without traverse.

If the Examiner believes that there are any points which may be clarified or otherwise disposed of, either by telephone discussion or by personal interview, the

Examiner is invited to contact applicants' undersigned attorney at the number indicated below.

If any costs are due in connection with the filing of this paper, please charge them to the Deposit Account of Antonelli, Terry, Stout & Kraus, LLP, Dep. Acct. No. 01-2135 (520.43306X00), and please credit any excess fees to such deposit account.

Respectfully submitted,
ANTONELLI, TERRY, STOUT & KRAUS, LLP



Gregory E. Montone
Reg. No. 28,141

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703-312-6600
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